## FIG.1

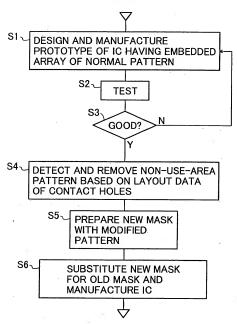


FIG.2

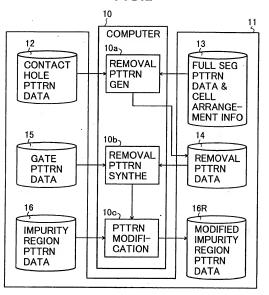
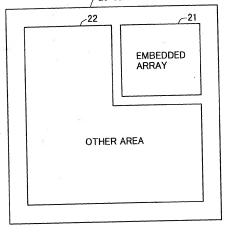
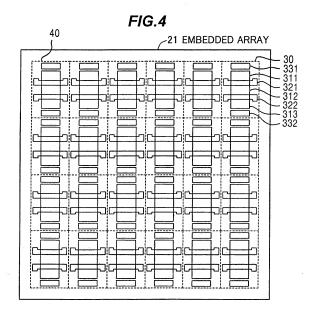


FIG.3
20 SEMICONDUCTOR CHIP





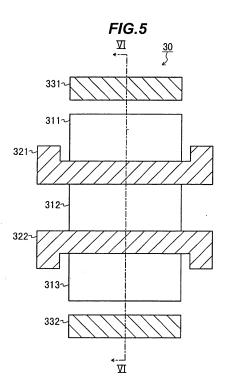


FIG.6

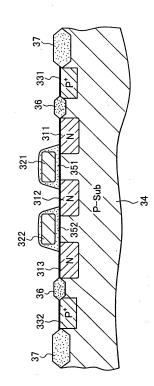


FIG.7

40 BASIC CELL PTTRN

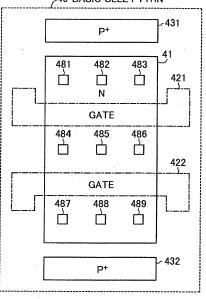


FIG.8

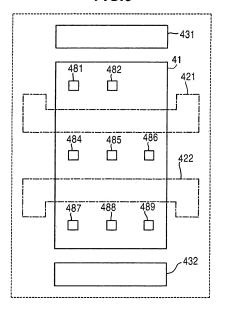
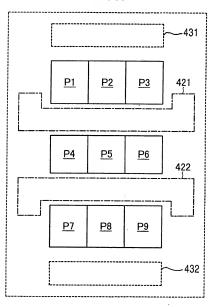


FIG.9





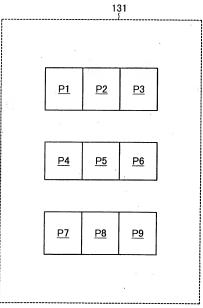


FIG.11

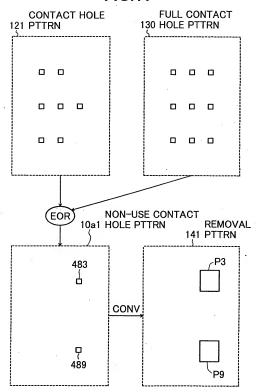


FIG.12

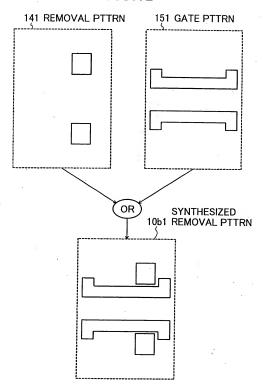
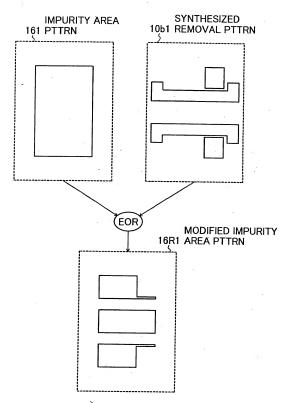


FIG.13



**FIG.14** 

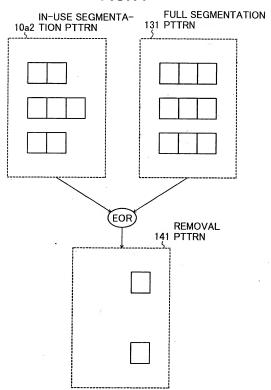


FIG.15

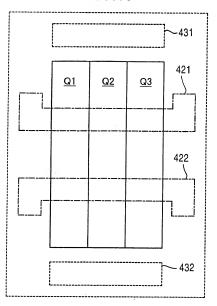


FIG.16

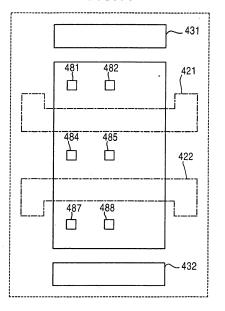


FIG.17

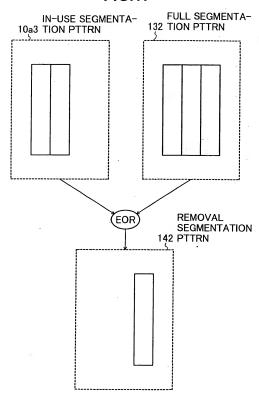


FIG.18

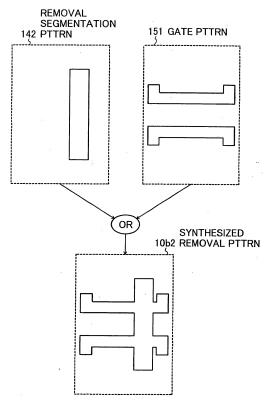


FIG.19

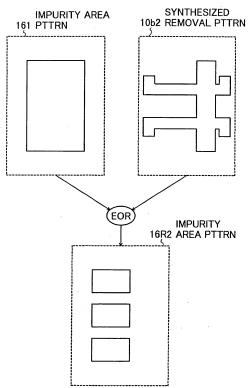


FIG.20

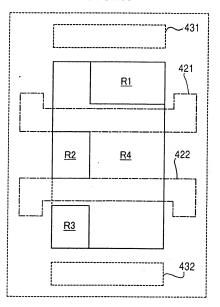


FIG.21

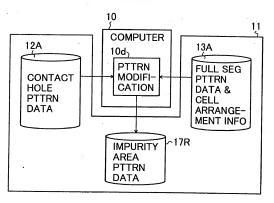


FIG.22

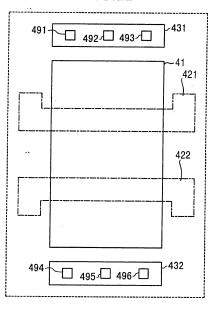


FIG.23

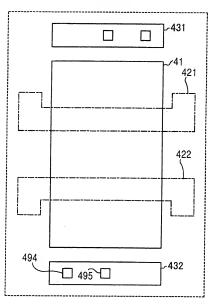


FIG.24

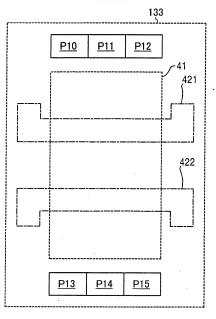


FIG.25

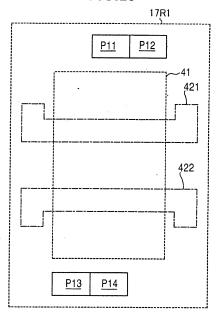


FIG.26

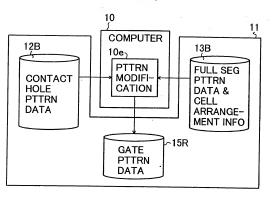


FIG.27

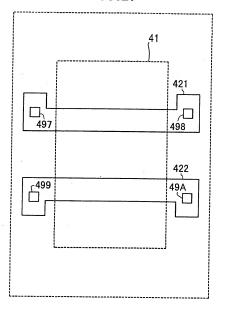


FIG.28

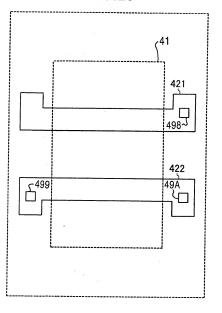


FIG.29

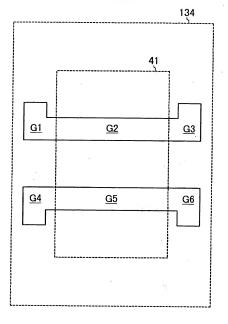


FIG.30 15R1

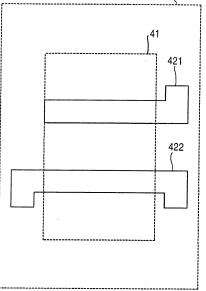


FIG.31

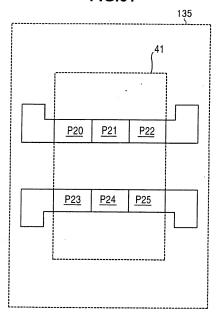


FIG.32 prior art

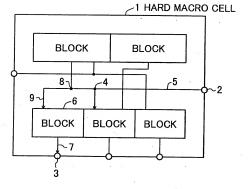


FIG.33 prior art

